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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10085255	FILING DATE 02/26/2002	CLASS 438	SUBCLASS 2812	GAU 2812	EXAMINER H. J. ...
<b>**APPLICANTS:</b> Altman Michael;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 42390.P13739	
TITLE : Physically defined varactor in a CMOS process					

U.S. DEPT. OF COMM. / PAT. & TM. PTO-435L (Rev. 12)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	
		Print Claim for O.G.	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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